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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,553	07/02/2003	Stefano S. Oggioni	FR920020056US1	6676
26502	7590	06/13/2005	EXAMINER	
IBM CORPORATION IPLAW IQ0A/40-3 1701 NORTH STREET ENDICOTT, NY 13760			NORRIS, JEREMY C	
			ART UNIT	PAPER NUMBER
			2841	

DATE MAILED: 06/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No. 10/613,553	Applicant(s) OGGIONI ET AL	
	Examiner Jeremy C. Norris	Art Unit 2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 March 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-11 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 March 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

Claims 4-11 are objected to because of the following informalities: Claim 4 is haphazardly constructed. There is a potential inconsistency where the first track apparently is stated to conduct signals as well as power/ground. Applicant is invited to clarify. For examination purposes, Examiner assumes the first and second conductive tracks to be power/ground tracks, whereas the third and fourth tracks are signal bearing tracks. Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-11 are rejected under 35 U.S.C. 102(e) as being anticipated by US 6,479,764 (hereafter Frana).

Frana discloses, referring to figure 4, a coaxial via structure in a circuit board having first and second surface thereon comprising: a first conductive via (10) for conveying only signal between said first and second surfaces; a second conductive via (6) spaced apart from and surrounding a substantial portion of said first conductive via wherein signals are not conveyed; and dielectric material (108) disposed between said

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first conductive via and said second conductive via [claim 1], wherein said second conductive via is the shape of an open ended cylinder (see fig. 1A) [claim 2], wherein said first and second conductive vias have a common axis (see fig. 1b) [claim 3].

Similarly, Frana discloses, referring to figure 7, a coaxial via structure in an electronic device carrier adapted to connect signals from a third conductive track (182) of a conductive layer on a surface of a core to a fourth conductive track (184) of another conductive layer on an opposite surface of said core a first (192) and second (194) conductive track, dielectric layer (108) being disposed between said each conductive layers, said coaxial via structure comprising: first conductive via (180) connected to said third and fourth conductive tracks; a second via (186) having a side wall with a conductive material thereon for ground or power connection surrounding a substantial portion of said first conductive said conductive material connected to said first and said other conductive tracks; and a dielectric material (190) disposed between said first conductive via and said conductive material on said side wall of said second via, said first and second conductive vias having a common axis, substantially perpendicular to said conductive layers (see fig. 4) [claim 4], wherein said core (190) of said electronic device carrier is disposed between said first and said other conductive layers [claim 5], further comprising a fifth conductive track on said third conductive layer, partially surrounding said third conductive track and being substantially aligned with at least one part of said first conductive track and at least a third conductive via connecting said fifth and said first conductive tracks (see fig. 7) [claim 6], further comprising a second conductive layer arranged between said first and third conductive layers, dielectric

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layers being disposed between said first conductive layer, said second conductive layer and said third conductive layer, respectively, wherein said second conductive layer comprises a second conductive track being substantially aligned with at least one part of said first conductive track and connected to said at least fourth conductive via (see fig. 3), further comprising a sixth conductive track on said third conductive layer partially surrounding said third conductive track and being substantially aligned with at least one part of said second conductive track of said second conductive layer, and a fifth conductive via connecting said sixth conductive track and said second conductive track of said second conductive layer (see fig. 9) [claim 8], wherein said first conductive layer includes a first supplementary track and said second conductive layer includes a second supplementary conductive track being substantially aligned with at least a part of said first supplementary conductive track and connected to said first supplementary conductive track by at least two supplementary conductive vias (see fig. 9) [claim 9], wherein said second conductive track at least partially surrounds said second supplementary conductive track (see fig 9) [claim 10], wherein said first conductive track at least partially surrounds said first supplementary conductive track [claim 11].

Response to Arguments

Applicant's arguments with respect to claims 1-11 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

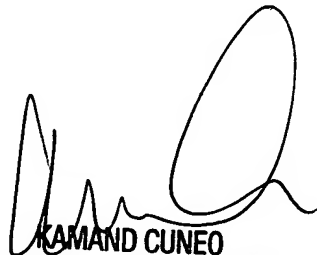
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is 571-272-1932. The examiner can normally be reached on Monday - Friday, 9:30 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JCSN



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SUPERVISORY PATENT EXAMINER
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